

CLAIM AMENDMENTS:

Please add new claims 13-14, as follows:

1. (Original) A liquid crystal display (LCD) panel, comprising:
an upper substrate;
a lower substrate below the upper substrate, wherein there is a first covering layer on the lower substrate and a second covering layer on the first covering layer and wherein the second covering layer comprises at least an opening, which exposes a portion of the first covering layer; and
a sealant which is employed between the upper substrate and the lower substrate, wherein the sealant contacts with the second covering layer and also contact the portion of the first covering layer via the opening so that the upper substrate and the lower substrate are adhered.
2. (Original) The display panel according to claim 1, wherein the first covering layer is a passivation layer.
3. (Original) The display panel according to claim 2, wherein the passivation layer comprises silicon nitride (Si_3N_4).
4. (Original) The display panel according to claim 2, wherein the passivation layer comprises silicon dioxide (SiO_2).

5. (Original) The display panel according to claim 1, wherein the second covering layer is a photoresist (PR) layer.

6. (Original) The display panel according to claim 5, wherein the photoresist layer comprises organic materials.

7. (Original) The display panel according to claim 1, wherein the upper substrate is a color filter (CF) substrate.

8. (Original) The display panel according to claim 1, wherein the lower substrate is a thin-film transistor (TFT) substrate.

9. (Original) A liquid crystal display (LCD) panel, comprising:
a color filter (CF) substrate ;
a thin-film transistor (TFT) substrate below the CF substrate, wherein there is a passivation layer on the TFT substrate and a photoresist (PR) layer on the passivation layer and wherein the PR layer comprises a plurality of openings, which expose a portion of the passivation layer ; and

a sealant which is employed between the CF substrate and the TFT substrate, wherein the sealant contacts with the PR layer and also contact the portion of the passivation layer via the openings so that the TFT substrate and the CF substrate are adhered.

10. (Original) The display panel according to claim 9, wherein the passivation layer comprises silicon nitride (Si_3N_4).

11. (Original) The display panel according to claim 9, wherein the passivation layer comprises silicon dioxide (SiO_2).

12. (Original) The display panel according to claim 9, wherein the photoresist layer comprises organic materials.

13. (New) The display panel according to claim 1, wherein the opening covers 5% to 70% of an area where the sealant is applied.

14. (New) The display panel according to claim 9, wherein the opening covers 5% to 70% of an area where the sealant is applied.